

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS)

B.Tech I Year I Semester Regular & Supplementary Examinations May-2022

BASIC ELECTRICAL & ELECTRONICS ENGINEERING

(Mechanical Engineering)

Time: 3 hours

3

6

(Answer all Six Units 6 X 10 = 60 Marks)

PART-A UNIT-I

- **1 a** Write the derivation for equivalent resistance in series circuit.
 - b A 5 ohm, 10 ohm, 20 ohm, resistors are connected in series across 120V DC supply calculates Total Resistance, Total current, Voltage drop across each L4 5M resistor.

OR

2 Find the voltage across 30 ohm resistor and current across 30 ohm resistor given L2 5M circuit as shown below.



b Write the derivation of RMS Value of Alternating voltage L3 5M

UNIT-II

LJ 51V

Max. Marks: 60

L3

5M

aState Thevenin's theoremL12MbFind the Thevenin's equivalent circuit across AB for the circuit shown.L38M



OR

4	Write the constructional features of a DC machine with neat diagram.	L3	10M
	UNIT-III		
5	a Discuss about the principle of operation of DC motors	L5	5 M
	b Calculate the value of torque established by the armature of a 4-pole DC motor	L5	5 M
	having 774 conductors, 2 paths in parallel, 24mwb flux per pole when the total		
	armature current is 50A.		
	OR		

a Derive EMF equation of a transformer.
b A 100 kVA, 11000/400 V, 50 Hz transformer has 40 secondary turns. Calculate
L3 6M
4M the number of primary turns and primary and secondary currents.

Q.P. Code: 20EE0251

R20

PART-B UNIT-IV

7	a Distinguish between conductors, semiconductors and insulators.	L4	5M
	b Discuss why an intrinsic semiconductor is relatively a poor conductor	L3	5M
8	a Explain the working principle of Bridge Wave Rectifier. Draw its input and output waveforms with neat circuit diagram.	L2	5M
	b Explain the working principle of rectifiers with capacitor filter.	L2	5M
	UNIT-V		
9	a Discuss the operation of PNP transistor with neat diagram.	L1	5M
	b If the base current in a transistor is 20μ A when the emitter current is 6.4mA, what are the values of α and β ? Also calculate the collector current.	L4	5M
	OR		
10	Explain the Fixed Bias of a BJT with a neat diagram.	L2	10M
	- I daar aanaa aanta aanta daaraa UNIT-VI aasaa da baadada daaraa daaraa		
11	a With a neat diagram explain the Drain characteristics of N-channel JFET.	L2	5M
	b Compare between CS, CG, CD configuration of JFET.	L4	5M
	\mathbf{OR}		
12	a Compare between BJT and JFET.	L4	5M
	b Explain working principle of EMOSFET with neat diagram.	L2	5M
	*** END ***		

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